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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Paul Shirley et al.

Patent No.: 7,846,851

Issue Date: December 7, 2010

Title: METHOD AND APPARATUS FOR A TWO-STEP RESIST SOFT BAKE TO PREVENT ILD  
OUTGASSING DURING SEMICONDUCTOR PROCESSING

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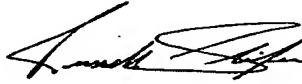
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Date: January 4, 2011

Respectfully submitted,  
  
By: \_\_\_\_\_  
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CERTIFICATE UNDER 37 CFR § 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450, on this 4th day of January 2011.

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